



श्री माता वैष्णो देवी विश्वविद्यालय
SHRI MATA VAISHNO DEVI UNIVERSITY
Kakryal, Katra – 182320 (J&K)
(A State University Recognized u/s 2(f) & 12(B) of UGC Act, 1956)

No. SMVDU/R&D/26/JDP-IITJ/2121-2126

Dated: 29.05.2026

NOTIFICATION

Sub:- Provisionally Shortlisted Candidate for appearing in Interview/ Presentation for admission to the Joint Degree Program (JDP) with IIT Jammu

The following candidate has been provisionally shortlisted by SRC (School Research Committee) of the School of Electronics and Communication Engineering at SMVDU for an interview/presentation scheduled to be held on 03.06.2026 for admission to the Joint Degree Program (JDP) with IIT Jammu for the Academic Year 2026-27 (odd semester):

S.no.	Name of the Candidate	Parentage	Remarks
1.	Mr. Aneesh Sharma	Sh. Anil Kumar Sharma	Shortlisted for appearing in the interview/ Presentation on 03.06.2026

The provisionally shortlisted candidate is required to appear in an Interview/Presentation on 3rd June, 2026 (Wednesday). The candidate is hereby informed to report for document verification and presentation/ Interview as per the details below:

S.no	Particulars	Date and Time	Coordination By:
1.	Reporting time and document verification of the candidate	10:00 am on 03.06.2026 in the AB Committee Room, Admin Block, SMVDU	Head of the School/ Chairman of JASC
2.	Presentation / Interview	11: 30 onwards on 03.06.2026	Head of the School/ Chairman of JASC

General Guidelines for the candidate:

1. The Candidate is required to bring original documents and 02 sets of self-attested photocopies (High School, Intermediate, Graduation, Post-Graduation, NET/SLET/GATE/M. Phil, Caste Certificate, etc.), in support of his claim as mentioned in the Ph.D. application form at the time of the interview.
2. He must carry a valid photo identification card like Aadhar/ Passport/Government Id card/ PAN card, etc., failing which he shall not be allowed to appear in the interview.
3. The candidate shall provide their project/research preference before the start of the Interview.
4. For any queries, the Head of the School of Electronics and Communication Engineering can be contacted through the registered email id and on the contact number (Registered email id and contact numbers are available on the University Website).

[Signature]
29/05/2026
Dean (R&D)

Copy to:

1. Registrar, SMVDU for information
2. Dean FoE for information
3. Head, SoECE, for information and circulation among all members of JASC for compliance
4. Assistant Registrar, Vice Chancellor's Secretariat, for the information of the Hon'ble Vice Chancellor.
5. I/c website, for uploading the same on the University website.
6. AR, Security for information and necessary action.
7. Concerned file.